



Test Report : MDD01L-15N

1W SIP Package DC-DC Medical Grade Unregulated Converter

■ DESIGN VERIFY TEST

Output Function Test

Input Function Test

■ SAFETY TEST

Safety Test

■ RELIABILITY TEST

Environment Test

Other Test

DESIGN VERIFY TEST
OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDICT
1	VOLTAGE ACCURACY	-5%~+5%	I/P:5VDC O/P:FULL LOAD Ta:25°C	-0.50% -0.49%	P
2	RIPPLE & NOISE	100 mVp-p	I/P:5VDC O/P:FULL LOAD Ta:25°C	20mV 21mV	P
3	LINE REGULATION	-12% ~ +12%	I/P:4.5VDC~5.5VDC O/P:FULL LOAD Ta:25°C	-10.45% ~ +10.22% -10.45% ~ +10.24%	P
4	LOAD REGULATION	-10% ~ +10%	I/P:5VDC O/P:MINI LOAD~FULL LOAD Ta:25°C	-1.94% ~ +1.91% -1.95% ~ +1.89%	P

INPUT FUNCTION TEST

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDICT
1	EFFICIENCY	82%	I/P:5VDC O/P:FULL LOAD Ta:25°C	82.96%	P
2	DC CURRENT	238 mA / FULL LOAD 28 mA / NO LOAD	I/P:5VDC O/P:NO / FULL LOAD Ta:25°C	235.5mA / FULL LOAD 26.96mA / NO LOAD	P

SAFETY TEST
SAFETY TEST

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDICT
1	WITHSTAND VOLTAGE	I/P-O/P:6000 VDC/min	I/P-O/P:6000 VDC/min Ta:25°C	I/P-O/P: 0.002mA NO DAMAGE	P
2	ISOLATION RESISTANCE	I/P-O/P:500VDC>100MΩ	I/P-O/P:500 VDC Ta:25°C	I/P-O/P>100MΩ NO DAMAGE	P

RELIABILITY TEST
ENVIRONMENT TEST

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDICT								
1	TEMPERATURE RISE TEST	1. ROOM AMBIENT BURN-IN : 8 HRS I/P:5VDC O/P:FULL LOAD Ta=25°C 2. HIGH AMBIENT BURN-IN : 8 HRS I/P:5VDC O/P:FULL LOAD Ta=90°C	<table border="1"> <thead> <tr> <th>NO</th> <th>Position</th> <th>1</th> <th>2</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>CASE</td> <td>36.0°C</td> <td>101.0°C</td> </tr> </tbody> </table>	NO	Position	1	2	1	CASE	36.0°C	101.0°C		P
NO	Position	1	2										
1	CASE	36.0°C	101.0°C										
2	LOW TEMPERATURE TURN ON TEST	TURN ON AFTER 2 HOURS	I/P:5VDC O/P: FULL LOAD Ta= -40°C	TEST : OK	P								

OTHER

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDICT
1	MTBF	MIL-HDBK-217F,GB,25°C,Full Load TOTAL FAILURE RATE : 0.0666 M.T.B.F : 15,000K HRS			P

TEST RESULT	TESTER	APPROVAL
PASS	ARCHEN HSIAO	PETER CHENG